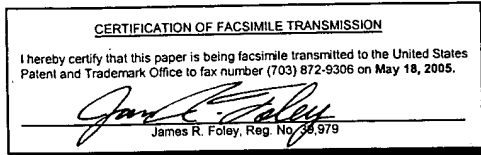


In re application of: James R. Foley et al.

Serial No.: 10/676,602

Filed: October 1, 2003

Art Unit: 2815

For: SUBSTRATE-BIASED I/O AND POWER ESD
PROTECTION CIRCUITS IN DEEP-
SUBMICRON TWIN-WELL PROCESSESBOX: AMENDMENT- NON FEE
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Transmitted herewith is a Response to the Office Action mailed March 24, 2005.

The filing fee has been calculated as shown below:

	(Col. 1)		(Col. 2)		(Col. 3)
	Claims Remaining After Amendment		Highest No. Previously Paid for		Present Extra
TOTAL	* 18	MINUS	** 20		0
INDEP.	* 2	MINUS	** 3		0
<input type="checkbox"/> FIRST PRESENTATION OF MULTIPLE DEP. CLAIM					

SMALL ENTITY	
Rate	Addit. Fee
x 25 =	\$.00
x 100 =	\$.00
+ 180 =	\$.00
TOTAL ADDIT. FEE	\$.00

OR

OTHER THAN A SMALL ENTITY	
Rate	Addit. Fee
x 50 =	\$.00
x 200 =	\$.00
+ 360 =	\$.00
TOTAL	\$.00

OR

- * If the entry in Col. 1 is less than the entry in Col. 2, write "0" in Col. 3.
 ** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, write "20" in this space.
 *** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, write "3" in this space.

The "Highest Number Previously Paid For" (Total or independent) is the highest number found from the equivalent box in Col. 1 of a prior amendment or the number of claims originally filed.

☐ Please charge my Deposit Account No. 12-2252 in the amount of \$ _____. A duplicate copy of this sheet is enclosed.

☐ A check in the amount of _____ to cover the filing fee is also enclosed.

☒ The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 12-2252.

☒ Any filing fees required under 37 CFR 1.16 for the presentation of extra claims.

☒ Any patent application processing fees under 37 CFR 1.17

Dated: May 18, 2005

James R. Foley
Attorney of Record

Reg. No. 39,979




IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE

PATENT

Serial No.: 10/676,602)
)
Inventors: Jau-Wen Chen et al.)
)
Filed: October 1, 2003)
)
For: **Substrate-Biased I/O and Power**)
ESD Protection Circuits in Deep-)
Submicron Twin-Well Processes)
)
Examiner: Matthew E. Warren)
)
Art Unit: 2815)
)
Atty. Ref.: 03-0717)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on **May 18, 2005**.


James R. Foley, Reg. No. 39,879

RESPONSE TO THE OFFICE ACTION OF MARCH 24, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In the matter of the above-identified application and in response to the Office Action mailed March 24, 2005, kindly enter the following amendments and consider the following remarks toward reconsideration of the present application.